

Notice of References Cited		Application/Control No.	Applicant(s)/Patent Under Reexamination 09/843,178 GOSIOR ET AL.	
		Examiner	Art Unit	Page 1 of 1 Aimee J. Li 2183

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-			
	B	US-			
	C	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Furber, Steve. "ARM System-on-Chip Architecture". Second Edition. Reading, Mass.: Pearson Education Limited, ©2000. Pages 19-20, 22-24, 28-32, 216-217, and 271.
	V	
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.